FIG. 1

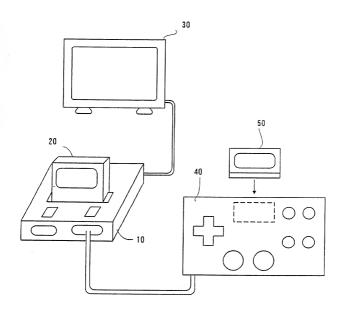


FIG. 2

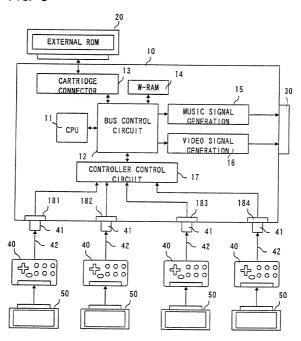


FIG. 3

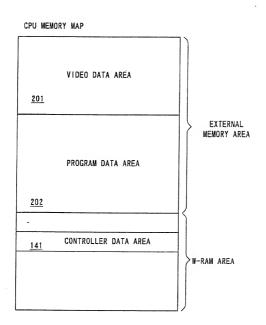


FIG. 4

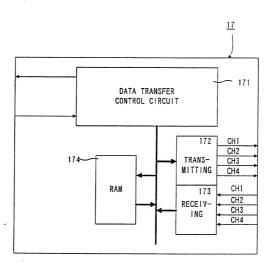
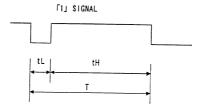


FIG. 5



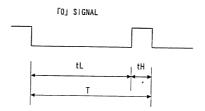


FIG. 6

RAM 174

1 CH (COMMAND	STORAGE	174a							
	ICH TRANSMISSION/RECEPTION DATA STORAGE									
174b										
2CH (COMMAND	STORAGE	174c							
		2CH		SMISSION/RECEPTION ATA STORAGE						
174d										
3CH C	COMMAND	STORAGE	174e							
2CH TRANSMISSION/RECEPTION DATA STORAGE										
174f										
4CH C	OMMAND	STORAGE	174g							
		2CH		MISSION/RECEPTION TA STORAGE						
174h										

FIG. 7

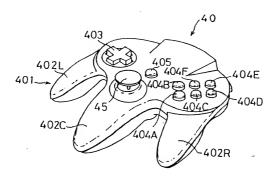
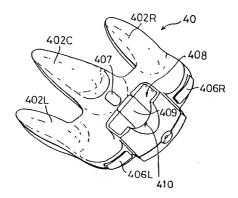


FIG. 8



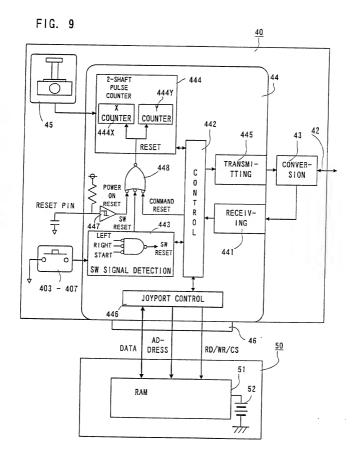


FIG. 10

1 BYTE	В	A	G	START	1		-				
2 BYTE	JSRST	0	L	R	E	D	С	F			
3 BYTE	X ORDINATE										
4 BYTE	Y ORDINATE										

FIG. 11

COMMAND 0: TRANSMITTING TYPE OF COMMAND

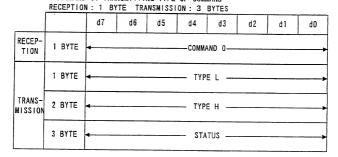
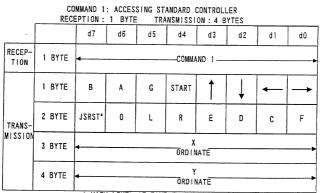


FIG. 12



* HIGH LEVEL AT TIME THAT L,R,START BUTTONS ARE SIMULTANEOUSLY DEPRESSED

FIG. 13

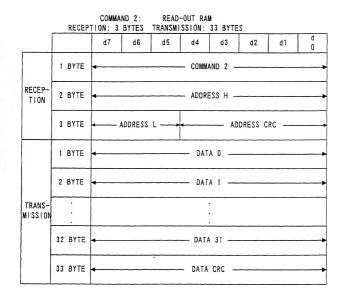


FIG. 14

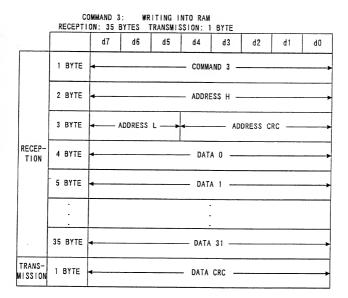


FIG. 15

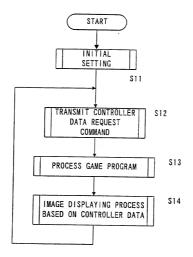
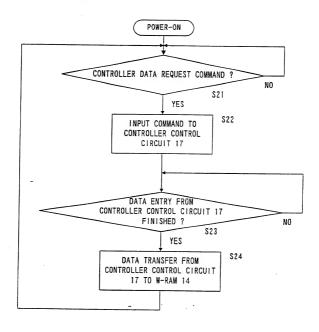
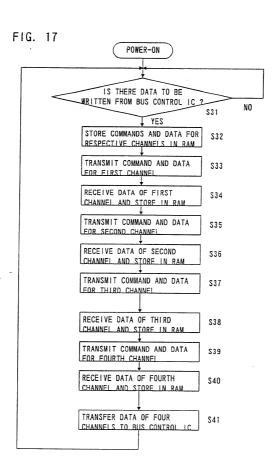


FIG. 16





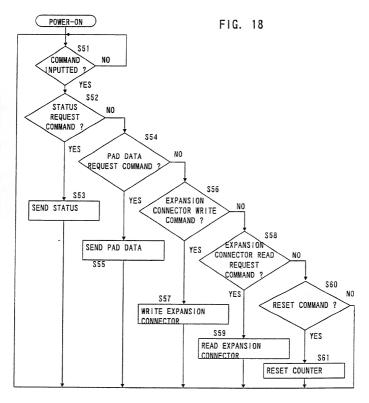
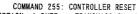


FIG. 19



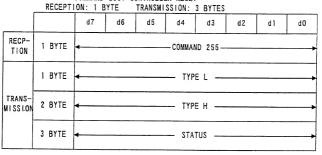


FIG. 20

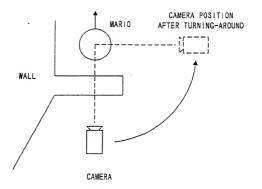


FIG. 21

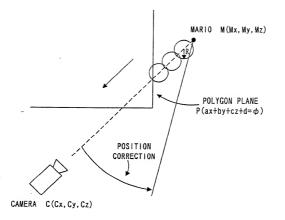


FIG. 22

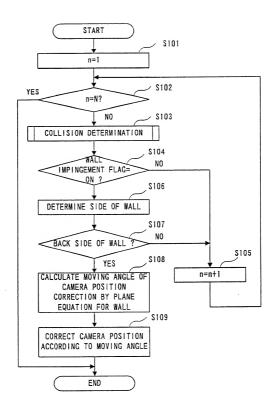


FIG. 23

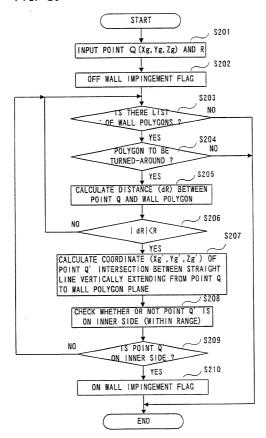


FIG. 24

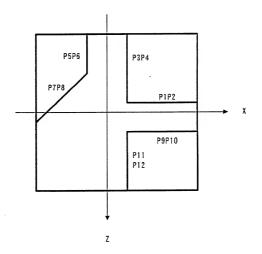


FIG. 25

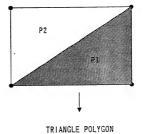


FIG. 26

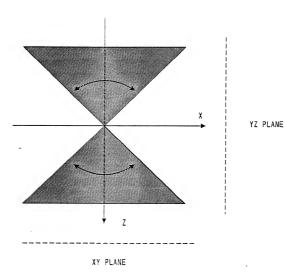


FIG. 27

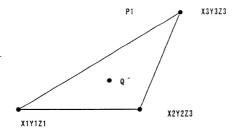


FIG. 28

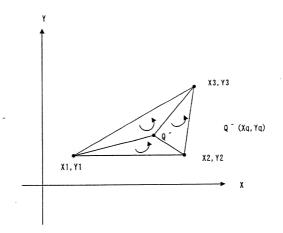


FIG. 29

